

Name: Professor David Lorge Parnas

Education Background:

1961 B.S. in Electrical Engineering (with honours), Carnegie Institute of Technology, Pittsburgh, Pa.

M.S. 1964 Electrical Engineering

Ph.D. 1965 Electrical Engineering - Systems & Communications Sciences

1986: “Doktor der technischen Wissenschaften -ehrenhalber”), granted by the Federal Institute of Technology (ETH) in Zurich Switzerland,

1996: Doctorate Honoris Causa, granted by Catholic University of Louvain (Belgium)

Research / work history:

Dr. Parnas has held teaching/research positions at Carnegie-Mellon University (USA), The University of Maryland (USA), The Technische Hochschule Darmstadt (Germany) , The University of North Carolina at Chapel Hill (USA), The Naval Research Laboratory (USA), the University of Victoria (Canada), McMaster University (Canada) and the University of Limerick

Research / work in SQRL:

Dr. Parnas is Directory of SQRL and responsible for the integrated programme of research there. He is interested in all aspects of software design and documentation.